

2811



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Inventor:

Horiuchi et al.

Title:

**Multi-layered Semiconductor** 

Device and Method for Producing the Same

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Art Unit:

2811

Examiner:

Douglas W. Owens

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Sir:

Enclosed and attached hereto are the following:

- 1) Cover Letter with Certificate of Mailing;
- 2) RCE Transmittal (1 page);
- 3) Check in the amount of \$750.00 for RCE;
- 4) One Month Petition for Extension of Time;
- 5) Check in the amount of \$110.00 for Extension of Time;
- 6) Amendment; and
- 7) Postcard to be date-stamped and returned.

PATENT Docket No. 149-01

No additional fees are believed to be required. In the event that an additional fee is required with respect to this communication, the Commissioner is hereby authorized to charge any additional fees, or credit any overpayment, to Paul & Paul Deposit Account No.16-0750.

Respectfully Submitted,

Date: June 16, 2003

Order No.: 1517

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